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**Inventor Name Search Result**

Your Search was:

Last Name = GEFFKEN

First Name = ROBERT

<b>Application#</b>	<b>Patent#</b>	<b>Status</b>	<b>Date Filed</b>	<b>Title</b>	<b>Inventor Name</b>
<u>60576439</u>	Not Issued	020	06/03/2004	DUAL DAMASCENE INTEGRATION STRUCTURE AND METHOD FOR FORMING IMPROVED DUAL DAMASCENE INTEGRATION STRUCTURE	GEFFKEN, ROBERT M.
<u>10687294</u>	Not Issued	030	10/16/2003	SINGLE AND MULTILEVEL REWORK	GEFFKEN, ROBERT M.
<u>10632653</u>	Not Issued	030	08/02/2003	CONTACT CAPPING LOCAL INTERCONNECT	GEFFKEN, ROBERT M.
<u>10605440</u>	Not Issued	030	09/30/2003	ADJUSTABLE SELF-ALIGNED AIR GAP DIELECTRIC FOR LOW CAPACITANCE WIRING	GEFFKEN, ROBERT M.
<u>10254277</u>	<u>6746947</u>	150	09/25/2002	POST-FUSE BLOW CORROSION PREVENTION STRUCTURE FOR COPPER FUSES	GEFFKEN, ROBERT M.
<u>10250147</u>	Not Issued	041	06/06/2003	FULL REMOVAL OF DUAL DAMASCENE METAL LEVEL	GEFFKEN, ROBERT M
<u>10248637</u>	Not Issued	041	02/03/2003	SACRIFICIAL METAL LINER FOR COPPER	GEFFKEN, ROBERT M.
<u>10248636</u>	Not Issued	041	02/03/2003	SACRIFICIAL METAL LINER FOR COPPER	GEFFKEN, ROBERT M.
<u>10248452</u>	<u>6674168</u>	150	01/21/2003	SINGLE AND MULTILEVEL REWORK	GEFFKEN, ROBERT M.
<u>10202134</u>	Not Issued	093	07/24/2002	SACRIFICIAL METAL SPACER DAMASCENE PROCESS	GEFFKEN, ROBERT M.
<u>10186814</u>	Not Issued	080	07/01/2002	METAL SPACER IN SINGLE AND DUAL DAMASCENE PROCESSING	GEFFKEN, ROBERT M.
<u>09854890</u>	<u>6426558</u>	150	05/14/2001	METALLURGY FOR SEMICONDUCTOR DEVICES	GEFFKEN, ROBERT M.
<u>09764504</u>	<u>6548338</u>	150	01/17/2001	INTEGRATED HIGH-PERFORMANCE DECOUPLING CAPACITOR AND	GEFFKEN, ROBERT M.

HEAT SINK				
<a href="#"><u>09745047</u></a>	<a href="#"><u>6680514</u></a>	150	12/20/2000	CONTACT CAPPING LOCAL INTERCONNECT
<a href="#"><u>09605806</u></a>	<a href="#"><u>6503827</u></a>	150	06/28/2000	METHOD OF REDUCING PLANARIZATION DEFECTS
<a href="#"><u>09557833</u></a>	Not Issued	083	04/26/2000	CORRECTION OF DEFECTS IN PLANARIZED SUBSTRATES
<a href="#"><u>09553581</u></a>	<a href="#"><u>6300236</u></a>	150	04/20/2000	COPPER STUD STRUCTURE WITH REFRACTORY METAL LINER
<a href="#"><u>09540737</u></a>	<a href="#"><u>6452251</u></a>	150	03/31/2000	DAMASCENE METAL CAPACITOR
<a href="#"><u>09527276</u></a>	<a href="#"><u>6495917</u></a>	150	03/17/2000	METHOD AND STRUCTURE OF COLUMN INTERCONNECT
<a href="#"><u>09526354</u></a>	<a href="#"><u>6426249</u></a>	150	03/16/2000	BURIED METAL DUAL DAMASCENE PLATE CAPACITOR
<a href="#"><u>09514511</u></a>	<a href="#"><u>6420263</u></a>	150	02/28/2000	METHOD FOR CONTROLLING EXTRUSIONS IN ALUMINUM METAL LINES AND THE DEVICE FORMED THEREFROM
<a href="#"><u>09388314</u></a>	<a href="#"><u>6498385</u></a>	150	09/01/1999	POST-FUSE BLOW CORROSION PREVENTION STRUCTURE FOR COPPER FUSES
<a href="#"><u>09283828</u></a>	<a href="#"><u>6236103</u></a>	150	03/31/1999	INTEGRATED HIGH-PERFORMANCE DECOUPLING CAPACITOR AND HEAT SINK
<a href="#"><u>09106980</u></a>	<a href="#"><u>6344373</u></a>	150	06/29/1998	A METHOD FOR FORMING AN ANTIFUSE
<a href="#"><u>09080573</u></a>	<a href="#"><u>6093630</u></a>	150	05/18/1998	SEMI-CONDUCTOR PERSONALIZATION STRUCTURE AND METHOD
<a href="#"><u>09004557</u></a>	<a href="#"><u>6180498</u></a>	150	01/08/1998	ALIGNMENT TARGETS HAVING ENHANCED CONTRAST
<a href="#"><u>08941857</u></a>	<a href="#"><u>6150723</u></a>	150	09/30/1997	COPPER STUD STRUCTURE WITH REFRACTORY METAL LINER
<a href="#"><u>08925655</u></a>	<a href="#"><u>5795819</u></a>	150	09/09/1997	INTEGRATED PAD AND FUSE STRUCTURE FOR PLANAR COPPER METALLURGY
<a href="#"><u>08858139</u></a>	<a href="#"><u>5985762</u></a>	150	05/19/1997	METHOD OF FORMING A SELF-ALIGNED COPPER DIFFUSION BARRIER IN VIAS
<a href="#"><u>08850033</u></a>	<a href="#"><u>5811870</u></a>	150	05/02/1997	ANTIFUSE STRUCTURE

					ROBERT M.
<u>08801427</u>	<u>5898227</u>	150	02/18/1997	ALIGNMENT TARGETS HAVING ENHANCED CONTRAST	GEFFKEN , ROBERT MICHAEL
<u>08720852</u>	<u>5719070</u>	150	10/02/1996	METALLIZATION COMPOSITE HAVING NICKEL INTERMEDIATE/INTERFACE	GEFFKEN , ROBERT M.
<u>08686899</u>	<u>5883435</u>	150	07/25/1996	PERSONALIZATION STRUCTURE FOR SEMICONDUCTOR DEVICES	GEFFKEN , ROBERT M.
<u>08671903</u>	<u>5731624</u>	150	06/28/1996	INTEGRATED PAD AND FUSE STRUCTURE FOR PLANAR COPPER METALLURGY	GEFFKEN , ROBERT M.
<u>08557886</u>	<u>5585674</u>	150	11/14/1995	TRANSVERSE DIFFUSION BARRIER INTERCONNECT STRUCTURE	GEFFKEN , ROBERT M.
<u>08433276</u>	Not Issued	166	05/02/1995	METALLIZATION COMPOSITE HAVING NICKEL INTERMEDIATE/INTERFACE	GEFFKEN , ROBERT M.
<u>08428995</u>	<u>5488013</u>	150	04/26/1995	METHOD OF FORMING TRANSVERSE DIFFUSION BARRIER INTERCONNECT STRUCTURE	GEFFKEN , ROBERT M.
<u>08182310</u>	<u>5457345</u>	150	01/14/1994	METALLIZATION COMPOSITE HAVING NICKEL INTERMEDIATE/INTERFACE	GEFFKEN , ROBERT M.
<u>08169787</u>	Not Issued	166	12/20/1993	TRANSVERSE DIFFUSION BARRIER INTERCONNECT STRUCTURE	GEFFKEN , ROBERT M.
<u>08060125</u>	Not Issued	161	05/07/1993	PLANARIZED LOCAL INTERCONNECT AND VIAS FOR SEMICONDUCTOR DEVICES	GEFFKEN , ROBERT M.
<u>07984769</u>	<u>5339212</u>	150	12/03/1992	SIDEWALL DECOUPLING CAPACITOR	GEFFKEN , ROBERT M.
<u>07881097</u>	Not Issued	166	05/11/1992	METALLIZATION COMPOSITE HAVING NICKEL INTERMEDIATE/INTERFACE	GEFFKEN , ROBERT M.
<u>07708608</u>	<u>5126006</u>	150	05/31/1991	PLURAL LEVEL CHIP MASKING	GEFFKEN , ROBERT M.
<u>07164692</u>	Not Issued	161	03/07/1988	METHOD FOR FORMING HIGH DENSITY, OHMIC CONTACT MULTI-LEVEL METALLURGY FOR SEMICONDUCTOR DEVICES	GEFFKEN , ROBERT M.
<u>06269230</u>	<u>4423547</u>	150	06/01/1981	METHOD FOR FORMING DENSE MULTILEVEL	GEFFKEN , ROBERT M.

INTERCONNECTION  
METALLURGY FOR  
SEMICONDUCTOR DEVICES

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**PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = HORAK

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<a href="#">10752386</a>	Not Issued	030	01/06/2004	FORMATION OF A DISPOSABLE SPACER TO POST Dope A GATE CONDUCTOR	HORAK, DAVID V.
<a href="#">10732958</a>	Not Issued	030	12/11/2003	WRAP-AROUND GATE FIELD EFFECT TRANSISTOR	HORAK, DAVID VACLAV
<a href="#">10732953</a>	Not Issued	030	12/11/2003	SHALLOW TRENCH ISOLATION FILL BY LIQUID PHASE DEPOSITION OF SiO2	HORAK, DAVID VACLAV
<a href="#">10732951</a>	Not Issued	030	12/11/2003	SELECTIVE SYNTHESIS OF SEMICONDUCTING CARBON NANOTUBES	HORAK, DAVID VACLAV
<a href="#">10732582</a>	Not Issued	030	12/10/2003	FIELD EMISSION PHASE CHANGE DIODE MEMORY	HORAK, DAVID V.
<a href="#">10732580</a>	Not Issued	030	12/10/2003	PHASE CHANGE TIP STORAGE CELL	HORAK, DAVID V.
<a href="#">10732579</a>	Not Issued	030	12/10/2003	INTEGRATED CIRCUIT WITH UPSTANDING STYLUS	HORAK, DAVID V.
<a href="#">10707937</a>	Not Issued	041	01/27/2004	DELIBERATE SEMICONDUCTOR FILM VARIATION TO COMPENSATE FOR RADIAL PROCESSING DIFFERENCES, DETERMINE OPTIMAL DEVICE CHARACTERISTICS, OR PRODUCE SMALL PRODUCTION	HORAK, DAVID V.
<a href="#">10707894</a>	Not Issued	030	01/21/2004	LIQUID-FILLED BALLOONS FOR IMMERSION LITHOGRAPHY	HORAK, DAVID V.
<a href="#">10707726</a>	Not Issued	071	01/07/2004	VERTICAL CARBON NANOTUBE FIELD EFFECT TRANSISTOR	HORAK, DAVID V.
<a href="#">10707009</a>	Not Issued	030	11/13/2003	ALTERNATING PHASE MASK BUILT BY ADDITIVE FILM DEPOSITION	HORAK, DAVID V.

<u>10705577</u>	Not Issued	030	11/10/2003	ETCH SELECTIVITY ENHANCEMENT FOR TUNABLE ETCH RESISTANT ANTI-REFLECTIVE LAYER	HORAK, DAVID V.
<u>10703355</u>	Not Issued	030	11/07/2003	METHODS FOR FABRICATING A METAL-OXIDE-SEMICONDUCTOR DEVICE STRUCTURE AND METAL-OXIDE-SEMICONDUCTOR DEVICE STRUCTURES FORMED THEREBY	HORAK, DAVID VACLAV
<u>10632653</u>	Not Issued	030	08/02/2003	CONTACT CAPPING LOCAL INTERCONNECT	HORAK, DAVID V.
<u>10605766</u>	Not Issued	071	10/24/2003	METHOD OF FORMING GAS DIELECTRIC WITH SUPPORT STRUCTURE	HORAK, DAVID V.
<u>10604196</u>	Not Issued	030	06/30/2003	METHODS OF PLANARIZATION	HORAK, DAVID
<u>10454852</u>	Not Issued	071	06/05/2003	METHOD FOR SUPPORTING A BOND PAD IN A MULTILEVEL INTERCONNECT STRUCTURE AND SUPPORT STRUCTURE FORMED THEREBY	HORAK, DAVID VACLAV
<u>10448729</u>	Not Issued	071	05/30/2003	DUAL GATE LOGIC DEVICE	HORAK, DAVID V.
<u>10447876</u>	Not Issued	041	05/29/2003	METHOD OF FORMING AN INTEGRATED SEMICONDUCTOR DEVICE HAVING CO-PLANAR DEVICE SURFACES	HORAK, DAVID V.
<u>10443709</u>	<u>6713835</u>	150	05/22/2003	METHOD FOR MANUFACTURING A MULTI-LEVEL INTERCONNECT STRUCTURE	HORAK, DAVID VACLAV
<u>10159181</u>	<u>6653737</u>	150	05/31/2002	INTERCONNECTION STRUCTURE AND METHOD FOR FABRICATING SAME	HORAK, DAVID V.
<u>10141279</u>	<u>6797641</u>	150	05/08/2002	GATE OXIDE STABILIZATION BY MEANS OF GERMANIUM COMPONENTS IN GATE CONDUCTOR	HORAK, DAVID VACLAV
<u>10064316</u>	Not Issued	071	07/02/2002	SEMICONDUCTOR WITH CONTACT CONTACTING DIFFUSION ADJACENT GATE ELECTRODE	HORAK, DAVID V.
<u>09779215</u>	<u>6429045</u>	150	02/07/2001	STRUCTURE AND PROCESS FOR MULTI-CHIP CHIP ATTACH WITH REDUCED RISK OF ELECTROSTATIC DISCHARGE DAMAGE	HORAK, DAVID V.

<u>09772378</u>	<u>6656807</u>	150	01/30/2001	GROOVED PLANAR DRAM TRANSFER DEVICE USING BURIED POCKET	HORAK, DAVID
<u>09745047</u>	<u>6680514</u>	150	12/20/2000	CONTACT CAPPING LOCAL INTERCONNECT	HORAK, DAVID V.
<u>09718010</u>	<u>6436814</u>	150	11/21/2000	INTERCONNECTION STRUCTURE AND METHOD FOR FABRICATING SAME	HORAK, DAVID V.
<u>09713830</u>	Not Issued	071	11/15/2000	FET WITH T-SHAPED GATE	HORAK, DAVID VACLAV
<u>09713766</u>	<u>6358813</u>	150	11/15/2000	METHOD FOR INCREASING THE CAPACITANCE OF A SEMICONDUCTOR CAPACITOR	HORAK, DAVID V.
<u>09702338</u>	<u>6583462</u>	150	10/31/2000	VERTICAL DRAM HAVING METALLIC NODE CONDUCTOR	HORAK, DAVID VACLAV
<u>09686740</u>	<u>6531724</u>	150	10/10/2000	METHOD FOR FORMING BORDERLESS GATE STRUCTURES AND APPARATUS FORMED THEREBY	HORAK, DAVID V.
<u>09667652</u>	<u>6255158</u>	150	09/22/2000	MANUFACTURING A VERTICAL DYNAMIC RANDOM ACCESS MEMORY DEVICE	HORAK, DAVID V.
<u>09657968</u>	<u>6420748</u>	150	09/08/2000	BORDERLESS BITLINE AND WORDLINE DRAM STRUCTURE	HORAK, DAVID V.
<u>09657232</u>	<u>6627477</u>	150	09/07/2000	MEHTOD OF ASSEMBLING A PLURALITY OF SEMICONDUCTOR DEVICE HAVING DIFFERENT THICKNESS	HORAK, DAVID V.
<u>09651614</u>	<u>6258661</u>	150	08/30/2000	FORMATION OF OUT-DIFFUSED BITLINE BY LASER ANNEAL	HORAK, DAVID V.
<u>09605806</u>	<u>6503827</u>	150	06/28/2000	METHOD OF REDUCING PLANARIZATION DEFECTS	HORAK, DAVID V.
<u>09605322</u>	<u>6605534</u>	150	06/28/2000	SELECTIVE DEPOSITION OF A CONDUCTIVE MATERIAL	HORAK, DAVID V.
<u>09604901</u>	<u>6440801</u>	150	06/28/2000	NOVEL STRUCTURE FOR FOLDED ARCHITECTURE PILLAR MEMORY CELL	HORAK, DAVID V.
<u>09599783</u>	Not Issued	092	06/22/2000	METHOD FOR ETCHING A SEMICONDUCTOR SUBSTRATE USING GERMANIUM HARD MASK	HORAK, DAVID V.
<u>09565504</u>	<u>6323082</u>	150	05/05/2000	PROCESS FOR MAKING A DRAM CELL WITH THREE-SIDED GATE	HORAK, DAVID V.

				TRANSFER	
<a href="#"><u>09559363</u></a>	<a href="#"><u>6326275</u></a>	150	04/24/2000	DRAM CELL WITH VERTICAL CMOS TRANSISTOR	HORAK, DAVID V.
<a href="#"><u>09557833</u></a>	Not Issued	083	04/26/2000	CORRECTION OF DEFECTS IN PLANARIZED SUBSTRATES	HORAK, DAVID V.
<a href="#"><u>09537934</u></a>	Not Issued	161	03/29/2000	DEVICE CONTACT STRUCTURE AND METHOD FOR FABRICATING SAME	HORAK, DAVID V.
<a href="#"><u>09532187</u></a>	<a href="#"><u>6444402</u></a>	150	03/21/2000	METHOD OF MAKING DIFFERENTLY SIZED VIAS AND LINES ON THE SAME LITHOGRAPHY LEVEL	HORAK, DAVID V.
<a href="#"><u>09524677</u></a>	<a href="#"><u>6506653</u></a>	150	03/13/2000	METHOD USING DISPOSABLE AND PERMANENT FILMS FOR DIFFUSION AND IMPLANT DOPING	HORAK, DAVID V.
<a href="#"><u>09523839</u></a>	<a href="#"><u>6333229</u></a>	150	03/13/2000	METHOD FOR MANUFACTURING A FIELD EFFECT TRANSITOR (FET) HAVING MIS-ALIGNED-GATE STRUCTURE	HORAK, DAVID V.
<a href="#"><u>09523796</u></a>	<a href="#"><u>6342323</u></a>	150	03/13/2000	ALIGNMENT METHODOLOGY FOR LITHOGRAPHY	HORAK, DAVID VACLAV
<a href="#"><u>09523480</u></a>	<a href="#"><u>6344416</u></a>	150	03/10/2000	DELIBERATE SEMICONDUCTOR FILM VARIATION TO COMPENSATE FOR RADIAL PROCESSING DIFFERENCES, DETERMINE OPTIMAL DEVICE CHARACTERISTICS, OR PRODUCE SMALL PRODUCTION	HORAK, DAVID V.
<a href="#"><u>09494415</u></a>	<a href="#"><u>6261933</u></a>	150	01/31/2000	PROCESS FOR BUILDING BORDERLESS BITLINE, WORDLINE AND DRAM STRUCTURE	HORAK, DAVID V.
<a href="#"><u>09493929</u></a>	<a href="#"><u>6452265</u></a>	150	01/28/2000	MULTI-CHIP MODULE UTILIZING A NONCONDUCTIVE MATERIAL SURROUNDING THE CHIPS THAT HAS A SIMILAR COEFFICIENT OF THERMAL EXPANSION	HORAK, DAVID V.
<a href="#"><u>09401054</u></a>	<a href="#"><u>6441464</u></a>	150	09/22/1999	GATE OXIDE STABILIZATION BY MEANS OF GERMANIUM COMPONENTS IN GATE CONDUCTOR	HORAK, DAVID VACLAV

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<b>Inventor</b>	HORAK	DAVID
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**Inventor Name Search Result**

Your Search was:

Last Name = HORAK

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<a href="#">10904235</a>	Not Issued	019	01/01/0001	APPARATUS AND METHOD FOR CONTROLLING PROCESS NON-UNIFORMITY	HORAK, DAVID V.
<a href="#">10904200</a>	Not Issued	019	01/01/0001	IRRADIATION ASSISTED REACTIVE ION ETCHING	HORAK, DAVID V.
<a href="#">10892467</a>	Not Issued	030	07/15/2004	METHOD FOR FABRICATING STRAINED SEMICONDUCTOR STRUCTURES AND STRAINED SEMICONDUCTOR STRUCTURES FORMED THEREBY	HORAK, DAVID VACLAV
<a href="#">10879833</a>	Not Issued	030	06/29/2004	DUAL GATED FINFET GAIN CELL	HORAK, DAVID VACLAV
<a href="#">10879815</a>	Not Issued	030	06/29/2004	HORIZONTAL MEMORY GAIN CELLS	HORAK, DAVID VACLAV
<a href="#">10853177</a>	Not Issued	030	05/26/2004	VERTICAL DUAL GATE FIELD EFFECT TRANSISTOR	HORAK, DAVID V.
<a href="#">10808740</a>	Not Issued	030	03/25/2004	SYSTEM AND APPARATUS FOR PHOTOLITHOGRAPHY	HORAK, DAVID VACLAV
<a href="#">10799819</a>	<a href="#">6815737</a>	150	03/12/2004	METHOD FOR SELECTIVE TRIMMING OF GATE STRUCTURES AND APPARATUS FORMED THEREBY	HORAK, DAVID V.
<a href="#">10798908</a>	Not Issued	030	03/11/2004	METHODS OF FORMING ALTERNATING PHASE SHIFT MASKS HAVING IMPROVED PHASE-SHIFT TOLERANCE	HORAK, DAVID VACLAV
<a href="#">10787640</a>	Not Issued	030	02/26/2004	INTEGRATED CIRCUIT CHIP UTILIZING CARBON NANOTUBE COMPOSITE	HORAK, DAVID VACLAV

INTERCONNECTION VIAS				
<u>10777576</u>	Not Issued	020	02/12/2004	METHODS OF FABRICATING VERTICAL CARBON NANOTUBE FIELD EFFECT TRANSISTORS FOR ARRANGEMENT IN ARRAYS AND FIELD EFFECT TRANSISTORS AND ARRAYS FORMED THEREBY
<u>10767065</u>	Not Issued	030	01/29/2004	VERTICAL NANOTUBE SEMICONDUCTOR DEVICE STRUCTURES AND METHODS OF FORMING THE SAME
<u>10767039</u>	Not Issued	030	01/29/2004	VERTICAL FIELD EFFECT TRANSISTORS INCORPORATING SEMICONDUCTING NANOTUBES GROWN IN A SPACER-DEFINED PASSAGE
<u>10760500</u>	Not Issued	071	01/20/2004	METHOD FOR FORMING QUADRUPLE DENSITY SIDEWALL IMAGE TRANSFER (SIT) STRUCTURES
<u>10752386</u>	Not Issued	030	01/06/2004	FORMATION OF A DISPOSABLE SPACER TO POST DOPE A GATE CONDUCTOR
<u>10749638</u>	Not Issued	020	12/31/2003	MOVING LENS FOR IMMERSION OPTICAL LITHOGRAPHY
<u>10711764</u>	Not Issued	019	01/01/0001	LOW-K DIELECTRIC MATERIAL BASED UPON CARBON NANOTUBES AND METHODS OF FORMING SUCH LOW-K DIELECTRIC MATERIALS
<u>10711758</u>	Not Issued	020	10/04/2004	REDUCED MASK COUNT GATE CONDUCTOR DEFINITION
<u>10711458</u>	Not Issued	030	09/20/2004	SUB-LITHOGRAPHIC IMAGING TECHNIQUES AND PROCESSES
<u>10710675</u>	Not Issued	030	07/28/2004	BORDERLESS CONTACT STRUCTURES
<u>10710447</u>	Not Issued	020	07/12/2004	FEATURE DIMENSION DEVIATION CORRECTION SYSTEM, METHOD AND

PROGRAM PRODUCT					
<a href="#"><u>10709314</u></a>	Not Issued	030	04/28/2004	METHOD FOR FORMING NARROW GATE STRUCTURES ON SIDEWALLS OF A LITHOGRAPHICALLY DEFINED SACRIFICIAL MATERIAL	HORAK, DAVID V.
<a href="#"><u>10232072</u></a>	<a href="#"><u>6819841</u></a>	150	08/29/2002	SELF ALIGNED OPTICAL WAVEGUIDE TO OPTICAL FIBER CONNECTION SYSTEM	HORAK, DAVID V.
<a href="#"><u>10222035</u></a>	Not Issued	071	08/16/2002	METHODS USING DISPOSABLE AND PERMANENT FILMS FOR DIFFUSION AND IMPLANTATION DOPING	HORAK, DAVID V.
<a href="#"><u>10187579</u></a>	Not Issued	041	07/01/2002	METHOD OF MAKING SUB-LITHOGRAPHIC FEATURES	HORAK, DAVID V.
<a href="#"><u>10013086</u></a>	<a href="#"><u>6716647</u></a>	150	12/07/2001	DELIBERATE SEMICONDUCTOR FILM VARIATION TO COMPENSATE FOR RADIAL PROCESSING DIFFERENCES, DETERMINE OPTIMAL DEVICE CHARACTERISTICS, OR PRODUCE SMALL PRODUCTION	HORAK, DAVID V.
<a href="#"><u>10008092</u></a>	<a href="#"><u>6506660</u></a>	150	11/13/2001	SEMICONDUCTOR CAPACITOR WITH NANOSCALE FEATURES	HORAK, DAVID V.
<a href="#"><u>09992767</u></a>	<a href="#"><u>6749969</u></a>	150	11/14/2001	REVERSE TONE PROCESS FOR MASKS	HORAK, DAVID V.
<a href="#"><u>09989822</u></a>	Not Issued	030	11/20/2001	VAPOR PHASE ETCH TRIM STRUCTURE WITH TOP ETCH BLOCKING LAYER	HORAK, DAVID V.
<a href="#"><u>09944665</u></a>	<a href="#"><u>6798017</u></a>	150	08/31/2001	VERTICAL DUAL GATE FIELD EFFECT TRANSISTOR	HORAK, DAVID V.
<a href="#"><u>09879590</u></a>	<a href="#"><u>6596597</u></a>	150	06/12/2001	METHOD OF MANUFACTURING DUAL GATE LOGIC DEVICES	HORAK, DAVID V.
<a href="#"><u>09871926</u></a>	<a href="#"><u>6489207</u></a>	150	06/01/2001	METHOD OF DOPING A GATE AND CREATING A VERY SHALLOW SOURCE/DRAIN EXTENSION AND RESULTING SEMICONDUCTOR	HORAK, DAVID V.
<a href="#"><u>09854890</u></a>	<a href="#"><u>6426558</u></a>	150	05/14/2001	METALLURGY FOR	HORAK,

				SEMICONDUCTOR DEVICES	DAVID V.
<u>09847479</u>	Not Issued	092	05/02/2001	GATE LINewidth TAILORING AND CRITICAL DIMENSION CONTROL FOR SUB-100 NM DEVICES USING PLASMA ETCHING	HORAK, DAVID V.
<u>09824896</u>	Not Issued	161	04/03/2001	PROCESS OF MANUFACTURING A DYNAMIC RANDOM ACCESS MEMORY DEVICE	HORAK, DAVID V.
<u>09793517</u>	<u>6429080</u>	150	02/27/2001	MULTI-LEVEL DRAM TRENCH STORE UTILIZING TWO CAPACITORS AND TWO PLATES	HORAK, DAVID V.
<u>09765561</u>	<u>6373091</u>	150	01/19/2001	VERTICAL DRAM CELL WITH TFT OVER TRENCH CAPACITOR	HORAK, DAVID VACLAV
<u>09394964</u>	<u>6333533</u>	150	09/10/1999	TRENCH STORAGE DRAM CELL WITH VERTICAL THREE-SIDED TRANSFER DEVICE	HORAK, DAVID V.
<u>09385929</u>	<u>6387596</u>	150	08/30/1999	METHOD OF FORMING RESIST IMAGES BY PERIODIC PATTERN REMOVAL	HORAK, DAVID V.
<u>09385907</u>	<u>6268908</u>	150	08/30/1999	MICRO ADJUSTABLE ILLUMINATION APERTURE	HORAK, DAVID VACLAV
<u>09384298</u>	<u>6228706</u>	150	08/26/1999	VERTICAL DRAM CELL WITH TFT OVER TRENCH CAPACITOR	HORAK, DAVID VACLAV
<u>09379940</u>	<u>6207540</u>	150	08/24/1999	METHOD FOR MANUFACTURING HIGH PERFORMANCE MOSFET DEVICE WITH RAISED SOURCE AND DRAIN	HORAK, DAVID V.
<u>09369412</u>	<u>6277543</u>	150	08/05/1999	METHOD FOR FORMING FEATURES USING FREQUENCY DOUBLING HYBIRD RESIST AND DEVICE FORMED THEREBY	HORAK, DAVID V.
<u>09369082</u>	<u>6342722</u>	150	08/05/1999	INTEGRATED CIRCUIT HAVING AIR GAPS BETWEEN DIELECTRIC AND CONDUCTING LINES	HORAK, DAVID V.
<u>09368843</u>	<u>6420766</u>	150	08/05/1999	TRANSISTOR HAVING RAISED SOURCE AND DRAIN	HORAK, DAVID V.
<u>09368767</u>	<u>6255178</u>	150	08/05/1999	METHOD FOR FORMING	HORAK,

				TRANSISTORS WITH RAISED SOURCE AND DRAINS AND DEVICE FORMED THEREBY	DAVID V.
<a href="#"><u>09299267</u></a>	<a href="#"><u>6387783</u></a>	150	04/26/1999	METHODS OF T-GATE FABRICATION USING A HYBRID RESIST	HORAK, DAVID V.
<a href="#"><u>09296807</u></a>	<a href="#"><u>6184549</u></a>	150	04/23/1999	TRENCH STORAGE DYNAMIC RANDOM ACCESS MEMORY CELL WITH VERTICAL TRANSFER DEVICE	HORAK, DAVID V.
<a href="#"><u>09281136</u></a>	<a href="#"><u>6037194</u></a>	150	03/29/1999	METHOD FOR MAKING A DRAM CELL WITH GROOVED TRANSFER DEVICE	HORAK, DAVID V.
<a href="#"><u>09275541</u></a>	<a href="#"><u>6184151</u></a>	150	03/24/1999	METHOD FOR FORMING CORNERED IMAGES ON A SUBSTRATE AND PHOTOMASK FORMED THEREBY	HORAK, DAVID V.
<a href="#"><u>09253455</u></a>	<a href="#"><u>6426175</u></a>	150	02/22/1999	FABRICATION OF A HIGH DENSITY LONG CHANNEL DRAM GATE WITH OR WITHOUT A GROOVED GATE	HORAK, DAVID V.

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## Inventor Name Search Result

Your Search was:

Last Name = HORAK

First Name = DAVID

<b>Application#</b>	<b>Patent#</b>	<b>Status</b>	<b>Date Filed</b>	<b>Title</b>	<b>Inventor Name</b>
<u>09479150</u>	<u>6338921</u>	150	01/07/2000	MASK WITH LINEWIDTHCOMPENSATION AND METHOD OF MAKING SAME	HORAK , DAVID V.
<u>09469275</u>	<u>6282115</u>	150	12/22/1999	MULTI-LEVEL DRAM TRENCH STORE UTILIZING TWO CAPACITORS AND TWO PLATES	HORAK , DAVID V.
<u>09469137</u>	<u>6333245</u>	150	12/21/1999	METHOD FOR INTRODUCING DOPANTS INTO SEMICONDUCTOR DEVICES USING A GERMANIUM OXIDE SACRIFICIAL LAYER	HORAK , DAVID V.
<u>09408353</u>	Not Issued	161	09/29/1999	METHOD OF REDUCING CAPACITANCE IN THE WIRING STRUCTURES OF SEMICONDUCTOR DEVICES	HORAK , DAVID V.
<u>09398659</u>	<u>6121128</u>	150	09/17/1999	METHOD FOR MAKING BORDERLESS WORDLINE FOR DRAM CELL	HORAK , DAVID V.
<u>09394964</u>	<u>6333533</u>	150	09/10/1999	TRENCH STORAGE DRAM CELL WITH VERTICAL THREE-SIDED TRANSFER DEVICE	HORAK , DAVID V.
<u>09374687</u>	<u>6153902</u>	150	08/16/1999	VERTICAL DRAM CELL WITH WORDLINE SELF-ALIGNED TO STORAGE TRENCH	HORAK , DAVID V.
<u>09369004</u>	<u>6245488</u>	150	08/05/1999	METHOD FOR FORMING FEATURES USING FREQUENCY DOUBLING HYBRID RESIST AND DEVICE FORMED THEREBY	HORAK , DAVID V.
<u>09296335</u>	<u>6297166</u>	150	04/22/1999	METHOD FOR MODIFYING NESTED TO ISOLATED OFFSETS	HORAK , DAVID V.

<u>09287410</u>	<u>6376873</u>	150	04/07/1999	VERTICAL DRAM CELL WITH ROBUST GATE-TO-STORAGE NODE ISOLATION	HORAK , DAVID V.
<u>09243221</u>	<u>6228705</u>	150	02/03/1999	OVERLAY PROCESS FOR FABRICATING A SEMICONDUCTOR DEVICE	HORAK , DAVID
<u>09192137</u>	<u>6221562</u>	150	11/13/1998	RESIST IMAGE REVERSAL BY MEANS OF SPUN-ON-GLASS	HORAK , DAVID V.
<u>09183338</u>	<u>6150256</u>	150	10/30/1998	METHOD FOR FORMING SELF-ALIGNED FEATURES	HORAK , DAVID V.
<u>09182173</u>	<u>6100172</u>	150	10/29/1998	METHOD FOR FORMING A HORIZONTAL SURFACE SPACER AND DEVICES FORMED THEREBY	HORAK , DAVID V.
<u>09175304</u>	<u>6090673</u>	150	10/20/1998	DEVICE CONTACT STRUCTURE AND METHOD FOR FABRICATING SAME	HORAK , DAVID V.
<u>09172865</u>	<u>6063658</u>	150	10/15/1998	METHODS OF MAKING A TRENCH STORAGE DRAM CELL INCLUDING A STEP TRANSFER DEVICE	HORAK , DAVID VACLAV
<u>09165503</u>	<u>5949700</u>	150	10/02/1998	FIVE SQUARE VERTICAL DYNAMIC RANDOM ACCESS MEMORY CELL	HORAK , DAVID V.
<u>09136586</u>	<u>6207493</u>	150	08/19/1998	FORMATION OF OUT-DIFFUSED BITLINE BY LASER ANNEAL	HORAK , DAVID V.
<u>09131828</u>	<u>6319759</u>	150	08/10/1998	METHOD FOR MAKING OXIDE	HORAK , DAVID V.
<u>09127132</u>	<u>6221680</u>	150	07/31/1998	PATTERNEDE RECESS FORMATION USING ACID DIFFUSION	HORAK , DAVID
<u>09126412</u>	<u>6121651</u>	150	07/30/1998	DRAM CELL WITH THREE-SIDED-GATE TRANSFER DEVICE	HORAK , DAVID V.
<u>09110487</u>	<u>6313492</u>	150	07/06/1998	FREQUENCY DOUBLING HYBRID PHOTORESIST	HORAK , DAVID V.
<u>09105739</u>	<u>6767789</u>	150	06/26/1998	METHOD FOR INTERCONNECTION BETWEEN TRANSFER DEVICES AND STORAGE CAPACITORS IN MEMORY CELLS AND DEVICE FORMED THEREBY	HORAK , DAVID V.
<u>09093928</u>	<u>6114725</u>	150	06/09/1998	STRUCTURE FOR FOLDED ARCHITECTURE PILLAR MEMORY CELL	HORAK , DAVID V.
<u>09092749</u>	<u>6614074</u>	150	06/05/1998	GROOVED PLANAR DRAM	HORAK ,

				TRANSFER DEVICE USING BURIED POCKET	DAVID
<u>09089650</u>	<u>6221704</u>	150	06/03/1998	PROCESS FOR FABRICATING SHORT CHANNEL FIELD EFFECT TRANSISTOR WITH A HIGHLY CONDUCTIVE GATE	HORAK , DAVID V.
<u>09086174</u>	<u>6190988</u>	150	05/28/1998	METHOD FOR A CONTROLLED BOTTLE TRENCH FOR A DRAM STORAGE NODE	HORAK , DAVID
<u>09086057</u>	<u>6225158</u>	150	05/28/1998	TRENCH STORAGE DYNAMIC RANDOM ACCESS MEMORY CELL WITH VERTICAL TRANFER DEVICE	HORAK , DAVID V.
<u>09085965</u>	<u>6107133</u>	150	05/28/1998	METHOD FOR MAKING A FIVE SQUARE VERTICAL DRAM CELL	HORAK , DAVID V.
<u>09082886</u>	<u>6014422</u>	250	05/21/1998	NOVEL METHOD FOR VARYING X-RAY HYBRID RESIST SPACE DIMENSIONS	HORAK , DAVID V.
<u>09078118</u>	<u>6184041</u>	150	05/13/1998	FUSED HYBRID RESIST SHAPES AS A MEANS OF MODULATING HYBRID RESIST SPACE WIDTH	HORAK , DAVID V.
<u>09072031</u>	<u>6210866</u>	150	05/04/1998	METHOD FOR FORMING FEATURES USING SELF-TRIMMING BY SELECTIVE ETCH AND DEVICE FORMED THEREBY	HORAK , DAVID V.
<u>09071704</u>	Not Issued	161	05/01/1998	METHOD OF DOPING A GATE AND CREATING A VERY SHALLOW SOURCE/DRAIN EXTENSION	HORAK , DAVID V.
<u>09071207</u>	<u>6100013</u>	150	05/01/1998	METHOD FOR FORMING TRANSISTORS WITH RAISED SOURCE AND DRAINS AND DEVICE FORMED THEREBY	HORAK , DAVID V.
<u>09070633</u>	<u>6017810</u>	150	04/30/1998	PROCESS FOR FABRICATING FIELD EFFECT TRANSISTOR WITH A SELF ALIGNED GATE TO DEVICE ISOLATION	HORAK , DAVID V.
<u>09056903</u>	<u>5945707</u>	150	04/07/1998	DRAM CELL WITH GROOVED TRANSFER DEVICE	HORAK , DAVID V.
<u>09052538</u>	<u>6175128</u>	150	03/31/1998	PROCESS FOR BUILDING BORDERLESS BITLINE, WORDLINE AND DRAM STRUCTURE AND RESULTING STRUCTURE	HORAK , DAVID V.
<u>09052403</u>	<u>6271555</u>	150	03/31/1998	BORDERLESS WORDLINE FOR	HORAK ,

				DRAM CELL	DAVID V.
<u>09024840</u>	<u>5998835</u>	150	02/17/1998	HIGH PERFORMANCE MOSFET DEVICE WITH RAISED SOURCE AND DRAIN	HORAK , DAVID V.
<u>09021924</u>	<u>6372412</u>	150	02/10/1998	METHOD OF PRODUCING AN INTEGRATED CIRCUIT CHIP USING FREQUENCY DOUBLING HYBRID PHOTORESIST AND APPARATUS FORMED THEREBY	HORAK , DAVID V.
<u>09014523</u>	<u>5831301</u>	150	01/28/1998	TRENCH STORAGE DRAM CELL INCLUDING A STEP TRANSFER DEVICE	HORAK , DAVID VACLAV
<u>08959779</u>	<u>6007968</u>	150	10/29/1997	METHOD FOR FORMING FEATURES USING FREQUENCY DOUBLING HYBRID RESIST AND DEVICE FORMED THEREBY	HORAK , DAVID V.
<u>08929730</u>	<u>5956597</u>	150	09/15/1997	METHOD FOR PRODUCING SOI & NON-SOI CIRCUITS ON A SINGLE WAFER	HORAK , DAVID V.
<u>08917009</u>	<u>5959325</u>	150	08/21/1997	METHOD FOR FORMING CORNERED IMAGES ON A SUBSTRATE AND PHOTOMASK FORMED THEREBY	HORAK , DAVID V.
<u>08878136</u>	<u>6391426</u>	150	06/19/1997	HIGH CAPACITANCE STORAGE NODE STRUCTURES	HORAK , DAVID V.
<u>08870336</u>	<u>5953607</u>	150	06/06/1997	NOVEL BURIED STRAP FOR TRENCH STORAGE CAPACITORS IN DRAM TRENCH CELLS	HORAK , DAVID V.
<u>08790876</u>	<u>5776660</u>	150	02/03/1997	FABRICATION METHOD FOR HIGH-CAPACITANCE STORAGE NODE STRUCTURES	HORAK , DAVID V.
<u>08771570</u>	Not Issued	161	12/20/1996	POLY-ALUMINUM EXCHANGE FOR ALUMINUM FILL OF HIGH ASPECT RATIO SHAPES	HORAK , DAVID V.
<u>08715287</u>	<u>6114082</u>	150	09/16/1996	FREQUENCY DOUBLING HYBRID PHOTORESIST HAVING NEGATIVE AND POSITIVE TONE COMPONENTS AND METHOD OF PREPARING THE SAME	HORAK , DAVID V.
<u>07635819</u>	<u>5173452</u>	150	01/02/1991	PROCESS FOR THE VAPOR DEPOSITION OF POLYSILANES PHOTORESISTS	HORAK , DAVID V.
<u>07510906</u>	Not Issued	161	04/18/1990	METHOD AND APPARATUS FOR	HORAK ,

		ENHANCING THE DEPTH OF FOCUS IN PROJECTION LITHOGRAPHY	DAVID V
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<b>Application#</b>	<b>Patent#</b>	<b>Status</b>	<b>Date Filed</b>	<b>Title</b>	<b>Inventor Name</b>
<a href="#"><u>09224760</u></a>	<a href="#"><u>6207514</u></a>	150	01/04/1999	METHOD FOR FORMING BORDERLSS GATE STUCTURES AND APPARATUS FORMED THEREBY	HORAK, DAVID V.
<a href="#"><u>09224759</u></a>	<a href="#"><u>6759315</u></a>	150	01/04/1999	METHOD FOR SELECTIVE TRIMMING OF GATE STRUCTURES AND APPARATUS FORMED THEREBY	HORAK, DAVID V.
<a href="#"><u>09192140</u></a>	<a href="#"><u>6303272</u></a>	150	11/13/1998	PROCESS FOR SELF-ALIGNMENT OF SUB-CRITICAL CONTACTS TO WIRING	HORAK, DAVID V.
<a href="#"><u>09183926</u></a>	<a href="#"><u>6194268</u></a>	150	10/30/1998	PRINTING SUBLITHOGRAPHIC IMAGES USING A SHADOW MANDREL AND OFF-AXIS EXPOSURE	HORAK, DAVID V.
<a href="#"><u>09182388</u></a>	<a href="#"><u>6096598</u></a>	150	10/29/1998	METHOD FOR FORMING PILLAR MEMORY CELLS AND DEVICE FORMED THEREBY	HORAK, DAVID V.
<a href="#"><u>09182173</u></a>	<a href="#"><u>6100172</u></a>	150	10/29/1998	METHOD FOR FORMING A HORIZONTAL SURFACE SPACER AND DEVICES FORMED THEREBY	HORAK, DAVID V.
<a href="#"><u>07704494</u></a>	Not Issued	163	05/23/1991	METHOD FOR ISOTROPIC DRY ETCHING OF A MULTI-LAYER STRUCTURE	HORAK, DAVID V.
<a href="#"><u>07311326</u></a>	Not Issued	166	02/15/1989	PROCESS FOR THE VAPOR DEPOSITION OF POLYSILANES	HORAK, DAVID V.

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Your Search was:

Last Name = STAMPER

First Name = ANTHONY

<b>Application#</b>	<b>Patent#</b>	<b>Status</b>	<b>Date Filed</b>	<b>Title</b>	<b>Inventor Name</b>
<a href="#"><u>10707811</u></a>	Not Issued	071	01/14/2004	SACRIFICIAL INORGANIC POLYMER INTERMETAL DIELECTRIC DAMASCENE WIRE AND VIA LINER	STAMPER, ANTHONY K.
<a href="#"><u>10707449</u></a>	Not Issued	041	12/15/2003	METHOD AND STRUCTURE FOR FORMING PRECISION MIM FUSIBLE CIRCUIT ELEMENTS USING FUSES AND ANTIFUSES	STAMPER, ANTHONY K.
<a href="#"><u>10691881</u></a>	Not Issued	020	10/23/2003	CHANGING AN ELECTRICAL RESISTANCE OF A RESISTOR	STAMPER, ANTHONY K.
<a href="#"><u>10688387</u></a>	Not Issued	030	10/17/2003	SELF-LOCKING WIRE BOND STRUCTURE AND METHOD OF MAKING THE SAME	STAMPER, ANTHONY K.
<a href="#"><u>10687294</u></a>	Not Issued	030	10/16/2003	SINGLE AND MULTILEVEL REWORK	STAMPER, ANTHONY K.
<a href="#"><u>10659778</u></a>	Not Issued	061	09/10/2003	DUAL DAMASCENE INTERCONNECT STRUCTURE USING LOW STRESS FLUOROSILICATE INSULATOR WITH COPPER CONDUCTORS	STAMPER, ANTHONY K.
<a href="#"><u>10640189</u></a>	Not Issued	071	08/13/2003	SPECIALLY SHAPED CONTACT VIA AND INTEGRATED CIRCUIT THEREWITH	STAMPER, ANTHONY KENDALL
<a href="#"><u>10636110</u></a>	Not Issued	090	08/07/2003	METHOD FOR CHANGING AN ELECTRICAL RESISTANCE OF A RESISTOR	STAMPER, ANTHONY K.
<a href="#"><u>10632653</u></a>	Not Issued	030	08/02/2003	CONTACT CAPPING LOCAL INTERCONNECT	STAMPER, ANTHONY K.
<a href="#"><u>10632652</u></a>	Not Issued	041	08/02/2003	APPARATUS AND METHOD FOR FORMING A BATTERY IN AN INTEGRATED CIRCUIT	STAMPER, ANTHONY K.
<a href="#"><u>10605926</u></a>	Not Issued	071	11/06/2003	METHOD FOR REDUCING AMINE BASED CONTAMINANTS	STAMPER, ANTHONY K.

<u>10604609</u>	Not Issued	030	08/04/2003	ELECTRICALLY TUNABLE ON-CHIP RESISTOR	STAMPER, ANTHONY KENDALL
<u>10604602</u>	Not Issued	094	08/04/2003	DAMASCENE INTERCONNECT STRUCTURES INCLUDING ETCHBACK FOR LOW-K DIELECTRIC MATERIALS	STAMPER, ANTHONY K.
<u>10604572</u>	Not Issued	094	07/31/2003	ELECTRICAL DETECTION OF Dicing DAMAGE	STAMPER, ANTHONY K.
<u>10604367</u>	Not Issued	030	07/15/2003	METHOD FOR REDUCING FOREIGN MATERIAL CONCENTRATIONS IN ETCH CHAMBERS	STAMPER, ANTHONY KENDALL
<u>10604136</u>	Not Issued	030	06/27/2003	DETECTION OF HARDMASK REMOVAL USING A SELECTIVE ETCH	STAMPER, ANTHONY K.
<u>10604134</u>	<u>6822472</u>	150	06/27/2003	DETECTION OF HARD MASK REMAINING ON A SURFACE OF AN INSULATING LAYER	STAMPER, ANTHONY K.
<u>10604026</u>	Not Issued	071	06/23/2003	DUAL DAMASCENE INTERCONNECT STRUCTURES HAVING DIFFERENT MATERIALS FOR LINE AND VIA CONDUCTORS	STAMPER, ANTHONY
<u>10382736</u>	<u>6664581</u>	150	03/05/2003	DAMASCENE CAPACITOR HAVING A RECESSED PLATE	STAMPER, ANTHONY K.
<u>10338624</u>	Not Issued	071	01/07/2003	DUAL DAMASCENE COPPER INTERCONNECT TO A DAMASCENE TUNGSTEN WIRING LEVEL	STAMPER, ANTHONY K.
<u>10250147</u>	Not Issued	041	06/06/2003	FULL REMOVAL OF DUAL DAMASCENE METAL LEVEL	STAMPER, ANTHONY K.
<u>10186814</u>	Not Issued	080	07/01/2002	METAL SPACER IN SINGLE AND DUAL DAMASCENE PROCESSING	STAMPER, ANTHONY K.
<u>10179285</u>	<u>6750114</u>	150	06/26/2002	ONE-MASK METAL-INSULATOR-METAL CAPACITOR AND METHOD FOR FORMING SAME	STAMPER, ANTHONY KENDALL
<u>10159181</u>	<u>6653737</u>	150	05/31/2002	INTERCONNECTION STRUCTURE AND METHOD FOR FABRICATING SAME	STAMPER, ANTHONY K.
<u>10063140</u>	<u>6452779</u>	150	03/25/2002	ONE-MASK METAL-INSULATOR-METAL CAPACITOR AND METHOD FOR FORMING SAME	STAMPER, ANTHONY KENDALL

<u>10054605</u>	Not Issued	133	11/13/2001	REDUCED ELECTROMIGRATION AND STRESSED INDUCED MIGRATION OF CU WIRES BY SURFACE COATING	STAMPER, ANTHONY KENDALL
<u>10047964</u>	Not Issued	164	01/15/2002	BILAYER HDP CVD / PE CVD CAP IN ADVANCED BEOL INTERCONNECT STRUCTURES AND METHOD THEREOF	STAMPER, ANTHONY K.
<u>10046595</u>	<u>6635548</u>	150	10/26/2001	CAPACITOR AND METHOD FOR FORMING SAME	STAMPER, ANTHONY K.
<u>09761123</u>	<u>6650000</u>	150	01/16/2001	APPARATUS AND METHOD FOR FORMING A BATTERY IN AN INTERGRATED CIRCUIT	STAMPER, ANTHONY K.
<u>09753284</u>	<u>6489663</u>	150	01/02/2001	SPIRAL INDUCTOR SEMICONDUCTING DEVICE WITH GROUNDING STRIPS AND CONDUCTING VIAS	STAMPER, ANTHONY K.
<u>09745047</u>	<u>6680514</u>	150	12/20/2000	CONTACT CAPPING LOCAL INTERCONNECT	STAMPER, ANTHONY K.
<u>09718010</u>	<u>6436814</u>	150	11/21/2000	INTERCONNECTION STRUCTURE AND METHOD FOR FABRICATING SAME	STAMPER, ANTHONY K.
<u>09712391</u>	<u>6730984</u>	150	11/14/2000	INCREASING AN ELECTRICAL RESISTANCE OF A RESISTOR BY OXIDATION OR NITRIDIZATION	STAMPER, ANTHONY K.
<u>09693765</u>	<u>6647614</u>	150	10/20/2000	METHOD OF CHANGING AN ELECTRICAL RESISTANCE OF A RESISTOR	STAMPER, ANTHONY K.
<u>09684000</u>	<u>6667533</u>	150	03/11/2002	TRIPLE DAMASCENE FUSE	STAMPER, ANTHONY K.
<u>09614234</u>	<u>6545330</u>	150	07/12/2000	ON CHIP ALPHA-PARTICLE DETECTOR	STAMPER, ANTHONY K.
<u>09573877</u>	Not Issued	161	05/19/2000	INTERCONNECTION WITH INTEGRATED CORROSION STOP	STAMPER, ANTHONY K.
<u>09564626</u>	<u>6362531</u>	150	05/04/2000	RECESSED BOND PAD	STAMPER, ANTHONY K.
<u>09557833</u>	Not Issued	083	04/26/2000	CORRECTION OF DEFECTS IN PLANARIZED SUBSTRATES	STAMPER, ANTHONY K.
<u>09541099</u>	Not Issued	168	03/31/2000	CAPACITOR STRUCTURE AND METHOD OF MAKING SAME	STAMPER, ANTHONY K.
<u>09540737</u>	<u>6452251</u>	150	03/31/2000	DAMASCENE METAL CAPACITOR	STAMPER, ANTHONY K.
<u>09527858</u>	<u>6368903</u>	150	03/17/2000	SOI LOW CAPACITANCE BODY	STAMPER,

				CONTACT	
<a href="#"><u>09527276</u></a>	<a href="#"><u>6495917</u></a>	150	03/17/2000	METHOD AND STRUCTURE OF COLUMN INTERCONNECT	ANTHONY K. STAMPER, ANTHONY K.
<a href="#"><u>09526984</u></a>	Not Issued	168	03/16/2000	SHIELDED INTERCONNECTOR FOR AN INTERGRATED CIRCUIT DEVICE	STAMPER, ANTHONY K.
<a href="#"><u>09526354</u></a>	<a href="#"><u>6426249</u></a>	150	03/16/2000	BURIED METAL DUAL DAMASCENE PLATE CAPACITOR	STAMPER, ANTHONY K.
<a href="#"><u>09525308</u></a>	<a href="#"><u>6444557</u></a>	150	03/14/2000	METHOD OF FORMING A DAMASCENE STRUCTURE USING A SACRIFICIAL CONDUCTIVE LAYER AND THE STRUCTURE SO FORMED	STAMPER, ANTHONY K.
<a href="#"><u>09525088</u></a>	<a href="#"><u>6680520</u></a>	150	03/14/2000	METHOD AND STRUCTURE OF A PRECISION MIM FUSIBLE CIRCUIT ELEMENTS USING FUSES AND ANTIFUSES	STAMPER, ANTHONY K.
<a href="#"><u>09514044</u></a>	<a href="#"><u>6303456</u></a>	150	02/25/2000	METHOD FOR MAKING A FINGER CAPACITOR WITH TUNEABLE DIELECTRIC CONSTANT	STAMPER, ANTHONY KENDALL
<a href="#"><u>09499577</u></a>	<a href="#"><u>6384468</u></a>	150	02/07/2000	CAPACITOR AND METHOD FOR FORMING SAME	STAMPER, ANTHONY K.
<a href="#"><u>09489277</u></a>	<a href="#"><u>6521977</u></a>	150	01/21/2000	DEUTERIUM RESERVOIRS AND INGRESS PATHS	STAMPER, ANTHONY K.
<a href="#"><u>09475712</u></a>	<a href="#"><u>6339022</u></a>	150	12/30/1999	METHOD OF ANNEALING COPPER METALLURGY	STAMPER, ANTHONY K.

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<b>Application#</b>	<b>Patent#</b>	<b>Status</b>	<b>Date Filed</b>	<b>Title</b>	<b>Inventor Name</b>
60429828	Not Issued	159	11/27/2002	DUMMY ARC APPLY REWORK VIA FIRST DUAL DAMASCENE	STAMPER, ANTHONY K.
<u>60399991</u>	Not Issued	159	07/31/2002	METHOD FOR REDUCING FOREIGN MATERIAL CONCENTRATIONS IN ETCH CHAMBERS	STAMPER, ANTHONY KENDALL
10849459	Not Issued	030	05/19/2004	SELF-ALIGNED CORROSION STOP FOR COPPER C4 AND WIREBOND	STAMPER, ANTHONY K.
<u>10844533</u>	Not Issued	030	05/13/2004	COPPER TO ALUMINUM INTERLAYER INTERCONNECT USING STUD AND VIA LINER	STAMPER, ANTHONY K.
10807029	Not Issued	041	03/23/2004	BILAYER HDP CVD / PE CVD CAP IN ADVANCED BEOL INTERCONNECT STRUCTURES AND METHOD THEREOF	STAMPER, ANTHONY K.
<u>10753241</u>	Not Issued	041	01/08/2004	INCREASING AN ELECTRICAL RESISTANCE OF A RESISTOR BY OXIDATION OR NITRIDIZATION	STAMPER, ANTHONY K.
10710828	Not Issued	020	08/05/2004	COPPER ALLOY VIA BOTTOM LINER	STAMPER, ANTHONY K.
<u>10710478</u>	Not Issued	030	07/14/2004	DUAL DAMASCENE WIRING AND METHOD	STAMPER, ANTHONY K.
<u>10710210</u>	Not Issued	030	06/25/2004	INTERCONNECT STRUCTURE IMPROVEMENTS	STAMPER, ANTHONY K.
<u>10710147</u>	Not Issued	030	06/22/2004	INTERLAYER CONNECTOR FOR PREVENTING DELAMINATION OF SEMICONDUCTOR DEVICE	STAMPER, ANTHONY K.

				AND METHODS OF FORMING SAME	
<u>10710034</u>	Not Issued	030	06/14/2004	PROCESS FOR INTERFACIAL ADHESION IN LAMINATE STRUCTURES THROUGH PATTERNED ROUGHING OF A SURFACE	STAMPER, ANTHONY K.
<u>10710000</u>	Not Issued	020	06/11/2004	VARIABLE CONTACT METHOD AND STRUCTURE	STAMPER, ANTHONY K.
<u>10709747</u>	Not Issued	030	05/26/2004	EXPOSED PORE SEALING POST PATTERNING	STAMPER, ANTHONY K.
<u>10709722</u>	Not Issued	019	05/25/2004	METHOD OF FORMING A SEMICONDUCTOR DEVICE HAVING AIR GAPS AND THE STRUCTURE SO FORMED	STAMPER, ANTHONY K.
<u>10709115</u>	Not Issued	030	04/14/2004	RESISTOR TUNING	STAMPER, ANTHONY K.
<u>10709109</u>	Not Issued	020	04/14/2004	STRUCTURE AND METHOD FOR PROVIDING PRECISION PASSIVE ELEMENTS	STAMPER, ANTHONY K.
<u>10709039</u>	Not Issued	030	04/08/2004	METHOD FOR MANUFACTURING SELF-COMPENSATING RESISTORS WITHIN AN INTEGRATED CIRCUIT	STAMPER, ANTHONY KENDALL
<u>10708883</u>	Not Issued	030	03/30/2004	FILLED CAVITIES SEMICONDUCTOR DEVICES	STAMPER, ANTHONY K.
<u>10708735</u>	Not Issued	030	03/22/2004	CRACKSTOP WITH RELEASE LAYER FOR CRACK CONTROL IN SEMICONDUCTORS	STAMPER, ANTHONY K.
<u>10707996</u>	Not Issued	030	01/30/2004	DEVICE AND METHODOLOGY FOR REDUCING EFFECTIVE DIELECTRIC CONSTANT IN SEMICONDUCTOR DEVICES	STAMPER, ANTHONY K.
<u>10702416</u>	Not Issued	030	11/06/2003	APPARATUS AND METHOD FOR LOW PRESSURE WIREBOND	STAMPER, ANTHONY K.
<u>10691881</u>	Not Issued	020	10/23/2003	CHANGING AN ELECTRICAL RESISTANCE OF A RESISTOR	STAMPER, ANTHONY K.
<u>10675177</u>	Not Issued	030	09/30/2003	TRIPLE DAMASCENE FUSE	STAMPER, ANTHONY K.
<u>10650890</u>	Not Issued	041	08/28/2003	BILAYER HDP CVD/PE CVD CAP IN ADVANCED BEOL INTERCONNECT	STAMPER, ANTHONY K.

				STRUCTURES AND METHOD THEREOF	
<u>10277835</u>	<u>6770501</u>	150	10/23/2002	DEUTERIUM RESERVOIRS AND INGRESS PATHS	STAMPER, ANTHONY K.
<u>10254277</u>	<u>6746947</u>	150	09/25/2002	POST-FUSE BLOW CORROSION PREVENTION STRUCTURE FOR COPPER FUSES	STAMPER, ANTHONY K.
<u>10248637</u>	Not Issued	041	02/03/2003	SACRIFICIAL METAL LINER FOR COPPER	STAMPER, ANTHONY K.
<u>10248636</u>	Not Issued	041	02/03/2003	SACRIFICIAL METAL LINER FOR COPPER	STAMPER, ANTHONY K.
<u>10248452</u>	<u>6674168</u>	150	01/21/2003	SINGLE AND MULTILEVEL REWORK	STAMPER, ANTHONY K.
<u>10248019</u>	<u>6797610</u>	150	12/11/2002	SUBLITHOGRAPHIC PATTERNING USING MICROTRENCHING	STAMPER, ANTHONY K.
<u>10246136</u>	<u>6645789</u>	150	09/18/2002	ON CHIP ALPHA-PARTICLE DETECTOR	STAMPER, ANTHONY K.
<u>10202134</u>	Not Issued	093	07/24/2002	SACRIFICIAL METAL SPACER DAMASCENE PROCESS	STAMPER, ANTHONY K.
<u>10195763</u>	<u>6677678</u>	150	07/15/2002	A DAMASCENE STRUCTURE USING A SACRIFICIAL CONDUCTIVE LAYER	STAMPER, ANTHONY K.
<u>10180777</u>	<u>6779711</u>	150	06/25/2002	SELF-ALIGNED CORROSION STOP FOR COPPER C4 AND WIREBOND	STAMPER, ANTHONY K.
<u>10005951</u>	<u>6650021</u>	150	12/03/2001	RECESSED BOND PAD	STAMPER, ANTHONY K.
<u>09996538</u>	<u>6420254</u>	150	11/28/2001	RECESSED BOND PAD	STAMPER, ANTHONY K.
<u>09996413</u>	<u>6624475</u>	150	11/29/2001	SOI LOW CAPACITANCE BODY CONTACT	STAMPER, ANTHONY K.
<u>09965289</u>	<u>6670255</u>	150	09/27/2001	METHOD OF FABRICATING LATERAL DIODES AND BIPOLAR TRANSISTORS	STAMPER, ANTHONY K.
<u>09878117</u>	Not Issued	094	06/08/2001	POLYSILICON CAPACITOR HAVING LARGE CAPACITANCE AND LOW RESISTANCE	STAMPER, ANTHONY K.
<u>09866937</u>	<u>6593660</u>	150	05/29/2001	PLASMA TREATMENT TO ENHANCE INORGANIC DIELECTRIC ADHESION TO COPPER	STAMPER, ANTHONY KENDALL
<u>09854890</u>	<u>6426558</u>	150	05/14/2001	METALLURGY FOR SEMICONDUCTOR DEVICES	STAMPER, ANTHONY K.

<a href="#"><u>09853956</u></a>	<a href="#"><u>6429524</u></a>	150	05/11/2001	ULTRA-THIN TANTALUM NITRIDE COPPER INTERCONNECT BARRIER	STAMPER, ANTHONY K.
<a href="#"><u>09832557</u></a>	<a href="#"><u>6522304</u></a>	150	04/11/2001	DUAL DAMASCENE HORN ANTENNA	STAMPER, ANTHONY KENDALL
<a href="#"><u>09816977</u></a>	<a href="#"><u>6566242</u></a>	150	03/23/2001	DUAL DAMASCENE COPPER INTERCONNECT TO A DAMASCENE TUNGSTEN WIRING LEVEL	STAMPER, ANTHONY K.
<a href="#"><u>09816278</u></a>	<a href="#"><u>6518643</u></a>	150	03/23/2001	TRI-LAYER DIELECTRIC FUSE CAP FOR LASER DELETION	STAMPER, ANTHONY K.
<a href="#"><u>09811965</u></a>	<a href="#"><u>6576525</u></a>	150	03/19/2001	DAMASCENE CAPACITOR HAVING A RECESSED PLATE	STAMPER, ANTHONY K.
<a href="#"><u>09811940</u></a>	<a href="#"><u>6563464</u></a>	150	03/19/2001	INTEGRATED ON-CHIP HALF-WAVE DIPOLE ANTENNA STRUCTURE	STAMPER, ANTHONY K.
<a href="#"><u>09805027</u></a>	Not Issued	041	03/12/2001	COPPER TO ALUMINUM INTERLAYER INTERCONNECT USING STUD AND VIA LINER	STAMPER, ANTHONY K.
<a href="#"><u>09788077</u></a>	<a href="#"><u>6653710</u></a>	150	02/16/2001	FUSE STRUCTURE WITH THERMAL AND CRACK-STOP PROTECTION	STAMPER, ANTHONY K.
<a href="#"><u>09764504</u></a>	<a href="#"><u>6548338</u></a>	150	01/17/2001	INTEGRATED HIGH-PERFORMANCE DECOUPLING CAPACITOR AND HEAT SINK	STAMPER, ANTHONY K.
<a href="#"><u>09681437</u></a>	Not Issued	168	04/04/2001	METHOD AND APPARATUS FOR MAKING AIR GAP INSULATION FOR SEMICONDUCTOR DEVICES	STAMPER, ANTHONY KENDALL

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#"><u>10904308</u></a>	Not Issued	019	01/01/0001	METHOD AND APPARATUS FOR CORRECTION OF DEFECTS IN LITHOGRAPHY MASKS	34 STAMPER, ANTHONY K.
<a href="#"><u>10711456</u></a>	Not Issued	030	09/20/2004	METHOD OF FABRICATING COPPER DAMASCENE AND DUAL DAMASCENE INTERCONNECT WIRING	STAMPER, ANTHONY K.
<a href="#"><u>10711394</u></a>	Not Issued	030	09/16/2004	AIR-GAP INSULATED INTERCONNECTIONS	STAMPER, ANTHONY K.
<a href="#"><u>10710828</u></a>	Not Issued	020	08/05/2004	COPPER ALLOY VIA BOTTOM LINER	STAMPER, ANTHONY K.
<a href="#"><u>09472346</u></a>	<a href="#"><u>6261951</u></a>	150	12/27/1999	PLASMA TREATMENT TO ENHANCE INORGANIC DIELECTRIC ADHESION TO COPPER	STAMPER , ANTHONY KENDALL
<a href="#"><u>09471587</u></a>	Not Issued	168	12/23/1999	METHOD OF FABRICATING DUAL DAMASCENE WIRING	STAMPER , ANTHONY K.
<a href="#"><u>09471065</u></a>	Not Issued	133	12/22/1999	SEMICONDUCTOR DEVICE CONNECTIONS AND METHODS FOR MAKING THE SAME	STAMPER , ANTHONY K.
<a href="#"><u>09467207</u></a>	Not Issued	161	12/20/1999	DUAL DAMASCENE INTERCONNECT STRUCTURE USING LOW STRESS FLOUROSILICATE INSULATOR WITH COPPER CONDUCTORS	STAMPER , ANTHONY K
<a href="#"><u>09412708</u></a>	<a href="#"><u>6297149</u></a>	150	10/05/1999	METHODS FOR FORMING METAL INTERCONNECTS	STAMPER , ANTHONY K.
<a href="#"><u>09388314</u></a>	<a href="#"><u>6498385</u></a>	150	09/01/1999	POST-FUSE BLOW CORROSION PREVENTION STRUCTURE FOR COPPER FUSES	STAMPER , ANTHONY K.

<u>09388163</u>	<u>6403393</u>	150	09/01/1999	A DEVICE HAVING INTEGRATED OPTICAL AND COPPER CONDUCTORS AND METHOD OF FABRICATING SAME	STAMPER , ANTHONY K.
<u>09388132</u>	<u>6433429</u>	150	09/01/1999	COPPER CONDUCTIVE LINE WITH REDUNDANT LINER AND METHOD OF MAKING	STAMPER , ANTHONY K
<u>09361573</u>	<u>6342733</u>	150	07/27/1999	REDUCED ELETROMIGRATION AND STRESSED INDUCED MIGRATION OF CU WIRES BY SURFACE COATING	STAMPER , ANTHONY KENDALL
<u>09311973</u>	<u>6457234</u>	150	05/14/1999	PROCESS FOR MANUFACTURING SELF-ALIGNED CORROSION STOP FOR COPPER C4 AND WIREBOND	STAMPER , ANTHONY K.
<u>09283828</u>	<u>6236103</u>	150	03/31/1999	INTEGRATED HIGH-PERFORMANCE DECOUPLING CAPACITOR AND HEAT SINK	STAMPER , ANTHONY K.
<u>09276160</u>	<u>6104079</u>	150	03/25/1999	CLOSELY PITCHED POLYSILICON FUSES AND METHOD OF FORMING THE SAME	STAMPER , ANTHONY K.
<u>09257372</u>	<u>6214730</u>	150	02/25/1999	FLUORINE BARRIER LAYER BETWEEN CONDUCTOR AND INSULATOR FOR DEGRADATION PREVENTION	STAMPER , ANTHONY K.
<u>09225530</u>	<u>6255217</u>	150	01/04/1999	PLASMA TREATMENT TO ENHANCE INORGANIC DIELECTRIC ADHESION TO COPPER	STAMPER , ANTHONY K.
<u>09225477</u>	<u>6331481</u>	150	01/04/1999	DAMASCENE ETCHBACK FOR LOW E DIELECTRIC	STAMPER , ANTHONY K.
<u>09225116</u>	<u>6734564</u>	150	01/04/1999	SPECIALLY SHAPED CONTACT VIA AND INTEGRATED CIRCUIT THEREWITH	STAMPER , ANTHONY KENDALL
<u>09225043</u>	<u>6261895</u>	150	01/04/1999	POLYSILICON CAPACITOR HAVING LARGE CAPACITANCE AND LOW RESISTANCE AND PROCESS FOR FORMING THE CAPACITOR	STAMPER , ANTHONY K.

<u>09224778</u>	<u>6319818</u>	150	01/04/1999	PATTERN FACTOR CHECKERBOARD FOR PLANARIZATION	STAMPER , ANTHONY K.
<u>09104074</u>	<u>5926738</u>	150	06/24/1998	INTERCONNECTS USING METAL SPACERS AND METHOD FOR FORMING SAME	STAMPER , ANTHONY K
<u>09066121</u>	<u>6111301</u>	150	04/24/1998	INTERCONNECTION WITH INTEGRATED CORROSION STOP	STAMPER , ANTHONY K.
<u>08937622</u>	<u>5930655</u>	150	09/25/1997	FLUORINE BARRIER LAYER BETWEEN CONDUCTOR AND INSULATOR FOR DEGRADATION PREVENTION	STAMPER , ANTHONY K.
<u>08937367</u>	<u>6066577</u>	150	09/25/1997	METHOD FOR PROVIDING FLUORINE BARRIER LAYER BETWEEN CONDUCTOR AND INSULATOR FOR DEGRADATION PREVENTION	STAMPER , ANTHONY K.
<u>08903439</u>	<u>5955773</u>	150	07/30/1997	CLOSELY PITCHED POLYSILICON FUSES AND METHOD OF FORMING THE SAME	STAMPER , ANTHONY K.
<u>08838371</u>	<u>5808364</u>	150	04/08/1997	INTERCONNECTS USING METAL SPACERS	STAMPER , ANTHONY K.
<u>08805023</u>	<u>5851903</u>	150	02/21/1997	METHOD OF FORMING CLOSELY PITCHED POLYSILICON FUSES	STAMPER , ANTHONY K.
<u>08744846</u>	<u>6310300</u>	150	11/08/1996	FLUORINE-FREE BARRIER LAYER BETWEEN CONDUCTOR AND INSULATOR FOR DEGRADATION PREVENTION	STAMPER , ANTHONY K.
<u>08744473</u>	<u>5773361</u>	250	11/06/1996	PROCESS OF MAKING A MICROCAVITY STRUCTURE AND APPLICATIONS THEREOF	STAMPER , ANTHONY K.
<u>08702082</u>	<u>5907763</u>	250	08/23/1996	METHOD AND DEVICE TO MONITOR INTEGRATED TEMPERATURE IN A HEAT CYCLE PROCESS	STAMPER , ANTHONY K.
<u>08699830</u>	Not Issued	166	08/20/1996	CLOSELY PITCHED POLYSILICON FUSES AND METHOD OF FORMING THE	STAMPER , ANTHONY K.

<u>08551092</u>	<u>5849629</u>	150	10/31/1995	SAME METHOD OF FORMING A LOW STRESS POLYCIDE CONDUCTORS ON A SEMICONDUCTOR CHIP	STAMPER , ANTHONY K.
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